**Anurag Dubey**

I studied ECE from BIT Mesra and graduated in the year 2013. I then worked as a Devops at Directi in Mumbai for a couple of years before heading to the USA to pursue MS. I continued my studies in embedded, network and electrical engineering at University of Colorado Boulder with primary research focus being Software Defined Networks. Since graduation, I've been working as FPGA-Profiling Engineer at Xilinx. So far, I have one paper and a patent publication.