

Matrusri Engineering College

(Sponsored by Matrusri Education Society, Estd.1980)
(Approved by AICTE & Affiliated to Osmania University)
#16-1-486, Saidabad, Hyderabad-500059. Ph: 040-24072764
(ISO 9001:2015 Certified)







ISVE RANCHI ZONAL CENTER- MATRUSRI HUB

A Two Day Faculty Development Program organized by the Department of Electrical & Electronics Engineering on

"Challenges & Opportunities in VLSI for Automotive Industry"

Chief Patron:

Dr P V Kashyap Chairman, MES

Patron:

Dr D Hanumantha Rao Principal, MECS

Zonal Coordinator:Dr Nookala Srinivas Rao

Head of the Department:
Dr G.Ravindranath

Coordinator: M.Saritha

Assistant Professor, EEE

Dates:

27-09-2024 to 28-09-2024

Online:
Online Mode

Registration fee:

- •Faculty ISVE Members: Rs400/-
- •Faculty Non-Members: Rs600/-
- ·Research Scholars/
- ISVE Student Members: Rs200/-
- •Research Scholars, Students Non-Members: Rs300/-
- •R&D Engineers:Rs3000/-

Last Date for Registration: 20.09.2024
Registration & Payment Link: https://forms.gle/ac29YYHF6

On-Line link will be shared prior to the program in WhatsApp group

For More Info:

Mobile: 8121489445

E-mail

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About ISVE Ranchi

Indian Society for VLSI Education (ISVE) is registered (Act21, 1860) non profitable society dedicated to serve the nation. It makes a bridge between industry and academia for organizing the workshops, summer/winter schools, short term courses, conferences, symposiums and seminars by which students, research scholars, faculties and scientists come together, work together and share their knowledge in recent development in engineering, science & technology.

Towards this end the society provides continuing technical education programmes for human resource development. It is committed to innovative research, publications in recent trends in electrical, electronics, computers, communications, environmental, biomedical & space engineering. The main aim of the society is to trend the manpower for electronics system design and manufacturing with the help of organizing the workshops, short term courses, symposiums, conferences etc.

About the College:

Matrusri Engineering College, Saidabad, Hyderabad, Telangana, was established by Matrusri Education Society in 2011 aiming at becoming a pioneer in Technical Education in the private sector. The college offers courses in Civil Engineering, Computer Science and Engineering, Electronics and Communication Engineering, Electrical and Electronics Engineering, Information Technology and Mechanical Engineering. The UG programs in engineering have been accredited by NBA. It is an ISO 9001:2015 certified Institution. The college runs with a team of highly qualified and experienced faculty members and state of the art equipment in all departments.

About the program:

Indian semiconductor Industry is in the growing stage. India needs manpower in the domain of semiconductor manufacturing design and verification. The following are the objectives of this FDP.

- o Deliver concepts of applications and emerging trends in semiconductor in light of design and verification.
- $_{\odot}$ To train faculty members in the front end VLSI design and verification flow.

Resource Persons: Identified from reputed Academic Institutions & Industry.

Target audience:

Faculty from Academic Institutions, Industry Personnel, Research Scholars and R&D Engineers.

Topics to be covered:

- VLSI design process for automotive applications
- •Role of VLSI in safety features infotainment and connectivity, electrification and power management.
- •Future VLSI in increased integration and applications of AI & \mathbf{ML} .