



DPU
UNIVERSITY



Winter School on Digital System Design with Verilog

Invitation for Inaugural & Technical Session

Members on Virtual Dais:

1. **Dr. D.K. Yadav**, Professor & Head, Department of CSE, NIT Jamshedpur & **President ISVE Ranchi**
2. **Dr. R.P. Yadav**, Professor (HAG) MNIT Jaipur & Former VC Rajasthan Technological University Kota & Brand Ambassador ISVE Ranchi (**Chief Guest 1**)
3. **Dr. Kavita Thakur**, Professor & Head, SoS in Electronics & Photonics, Pt Ravishankar Shukla University Raipur & **Coordinator WS-DSDV-2024 (Chief Guest 2)**
4. **Sh. K.K. Thakur**, Former CGMT, Jharkhand Circle, Ranchi & Brand Ambassador ISVE Ranchi (**Guest of Honor**)
5. **Dr. N. Srinivasa Rao**, Professor & Head, Dept of ECE, MEC, Hyderabad & **ISVE Zonal Coordinator, Hyderabad**
6. **Dr. Vijay Nath**, Associate Professor, Birla Institute of Technology, Ranchi & **Resource Person WS-DSDV-2024**
7. **Dr. Arun Kumar**, Former Director ECOM, University Department of Physics, Ranchi University Ranchi & **Chairman ISVE Ranchi**
8. **Dr. Anand Kr. Thakur**, Assistant Professor, Ranchi University Ranchi & **Organizing Secretary WS-DSDV-2024**
9. **Dr. Sagar A. More**, Associate Professor Department of Electronics, Bapusaheb Shivajirao Deore College of Engineering, Dhule, Maharashtra
10. **Dr. Raj Kumar Singh**, Assistant Professor, University Department of Physics, Ranchi University Ranchi & **Convener WS-DSDV-2024**

11. Dr. Nibha Desai, HoD, Department of Electronics Engineering (VLSI Design & Technology), Mahavir Education Trusts Shah and Anchor Kutchhi Engineering College Mumbai, Maharashtra

12. Mrs Saroj, Chairman, Dr Vijay Nath Educational Welfare Society(DVNEWS) Raghunathpur, Gorakhpur, Uttar Pradesh

13. Dr. Bhalke Daulappa Guranna, Professor & Head, Department of Electronics & Telecommunication Engineering, Dr. D.Y. Patil Institute of Engineering & Technology, Pimpri, Pune, Maharashtra

14. Dr. Mohini Sardey HoD, Electronics & Telecommunication Department, AISSMS Institute of Information Technology, Pune-411001, Maharashtra

15. Dr. Khaleelu Rahman, Associate Professor, Department of Electronics & Communication Engineering, Chaitanya Bharati Institute of Technology (CBIT) Hyderabad, Telangana & **Resource Person WS-DVDV-2024**

16. Dr. Adesh Kumar, Professor, Department of Electronics & Communication Engineering, UPES University Dehradun & **Resource Person WS-DVDV-2024**

17. Sh. Shreyaskumar Patel Sr. Software Engineer NetScout Systems Inc, IEEE,Anna, Texas, USA (Coordinator ISVE Ranchi Chapter, Texas, USA) & **Resource Person WS-DSDV-2024**

18. Dr Battula Tirumala Krishna, Professor Department of Electronics & Communication Engineering, UCEK, JNTU Kakinada (AP)

19. Mr. Samuel Tensingh, University of Sydney ,Australia(Coordinator ISVE Ranchi Chapter, Sydney, Australia) **Resource Person WS-DSDV-2024**

Links for Joining Inaugural & Technical Session:
<https://meet.google.com/fxp-yito-vqn?authuser=0&pli=1>

The ISVE with collaborative institutions are organizing **Winter School on Digital VLSI Design with Verilog** from 26-30th Dec 2024 in **online mode**.

Course Timing: From 6:00pm to 8:00pm

Who can attend?: Students from EEE/ECE/CSE/IT 2nd year onwards are eligible for this course.

Course Information Details: <https://isve.in/content/main/44>

Registration Form (Send the filled scan copy of registration form to e-mail: isve.ranchi@gmail.com):
https://www.isve.in/ContentUploaded/adminisve/files/Registration%20Form_Winter%20School-2024.pdf

Online Registration Link:

<https://docs.google.com/forms/d/e/1FAIpQLSdPdHbecXEED7f0R7PsRnXsoKbFpEfk4rOdyK-2LNWyNnPcvQ/viewform>

About the Verilog: This is HDL (Hardware Description Language) used to model the electronic circuits and systems. It is most commonly used in the design and verification of digital circuits at the register -transfer level of the abstractions. It is most demanding course in electronics industry.

About the Experts: This course will be taught by Academia and Industry experts.

Course Timing: Course will run daily in evening 6:00pm onwards by which college students and industry professionals can attend easily. Course will cover theory with practical.

Advanced Course with Placement: Advanced course will also be offered after this course and interested can opt and register, who wish to make their carrier in VLSI Design/Chip Design and verifications.

Contact us: For any help and clarification try to contact via mail : isve.ranchi@gmail.com