



Matrusri Engineering College

(An Autonomous Institution, Approved by AICTE,
Affiliated to Osmania University)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

One day Outreach Program on “Chip Designing and Computational Intelligence” with Inauguration of ISVE Zonal Centre at Matrusri Engineering College under aegis of India Semiconductor Mission

- **Title of the activity:** Chip Designing and Computational Intelligence
- **Program Type:** One day Outreach programme
- **Date(s):** 26th July 2024
- **Name of the Resource Person:** Dr. D. K. Yadav, Professor & Head, Department of CSE, National Institute of Technology, Jamshedpur (JH), President ISVE Ranchi; Dr. Vijay Nath, Associate Professor, VLSI Design Group, Department of ECE, Birla Institute of Technology, Mesra Ranchi, General Chair ISVE Ranchi; Sh. K. K. Thakur, Former CGMT, BSNL, Jharkhand.
- **No. of students participated:** 125
- **No. of Faculty participated:** 10



Dr K K Yadav delivering the lecture



Dr Vijay Nath delivering the lecture



Sh K K Thakur delivering the lecture























Faculty Coordinator

HOD