



INDIAN SOCIETY FOR VLSI EDUCATION
RANCHI-835217 (JHARKHAND) INDIA
Matusri Engineering College, Hydradabad



ISVE Summer Research Internship-2026

Digital System Design with Verilog/ Digital System Design with VHDL/ Embedded System Design & IoT / Chip Design/ Microelectronics & VLSI Design/ AI & ML/ Cloud Computing & Distributed Systems

Inauguration of Program: 15.05.2026

Google Meet link for Joining: <https://meet.google.com/hrd-xawe-mmm>

Introduction of Summer Internship	Dr. D.K. Yadav President ISVE Ranchi/ Executive Committee Members	6:00pm-6:10pm
Message from Chairman ISVE Ranchi	Dr. Arun Kumar Executive Committee Members & Former Director ECOM, RU	6:11pm-6:15pm
Message from Guest of Honor	Dr Kavita Thakur Professor, PRSU Raipur	6:16pm-6:20pm
Message from Zonal Mentor	Dr. N. Srinivasa Rao MEC Hyderabad	6:21pm-6:25pm
Message from Chief Guest	Sh. K. K. Thakur Former CGMT BSNL Jharkhand Circle Ranchi & Brand Ambassador of ISVE Ranchi	6:26pm-6:30pm
Summer Research Internship-2026 Zonal Coordinator Hyderabad (Telangana, AP)	Dr. Nukala Srinivasa Rao Professor & Head, ECE Matusri Engineering College Saidabad, Hyderabad	
General Chair	Dr. Vijay Nath BIT Mesra	
Coordinator	Dr. Raj Kumar Singh RU Ranchi	
Organizing Secretary	Dr. Anand Kumar Thakur RU Ranchi	
Convenor	Dr. Kavita Thakur, PRSU Raipur	



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Moderator	Dr. Divya Sharma & Mrs. Nikhat Anjum
For any query Contact at	isve.ranchi@gmail.com

Program Schedule

Duration of the Course	15 th May 2026	14 th June 2026 & Further	
Timing for starting the courses (Monday to Friday)	6:00pm		
Google Meet link for Joining: https://meet.google.com/hrd-xawe-mmm			
SN	Date	Name of Speaker	Topic of Internship
1	15.05.2026 (Friday)	Dr. Adesh Kumar UPES, UK	Digital System Design with VHDL
2	16.05.2026 (Saturday) to 17.05.2026 (Sunday)	Assignment Execution	
3	18.05.2026 (Monday)	Dr. Arpan Deyasi RCCIIT, Kolkata	Microelectronics & VLSI Design
4	19.05.2026 (Tuesday)	Dr. Khaleelu Rahman CBIT Hyderabad	Embedded System Design
5	20.05.2026 (Wednesday)	Dr. Vijay Nath BIT Mesra	Digital System Design with Verilog
6	21.05.2026 (Thursday)	Dr. Pokkuluri Kiran Sree Shri Vishnu Engineering College for Women, Bishnupur	Artificial Intelligence & Machine Learning
7	22.05.2026 (Friday)	Dr. Adesh Kumar UPES, UK	Digital System Design with VHDL
8	23.05.2026 (Saturday) to 24.05.2026 (Sunday)	Assignment Execution	
9	25.05.2026 (Monday)	Dr. Arpan Deyasi RCCIIT, Kolkata	Microelectronics & VLSI Design



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10	26.05.2026 (Tuesday)	Dr. Khaleelu Rahman CBIT Hyderabad	Embedded System Design & IoT
11	27.05.2026 (Wednesday)	Dr. Vijay Nath BIT Mesra	Digital System Design with Verilog
12	28.05.2026 (Thursday)	Dr. Pokkuluri Kiran Sree Shri Vishnu Engineering College for Women, Bishnupur	Artificial Intelligence & Machine Learning
13	29.05.2026 (Friday)	Dr. Adesh Kumar UPES, UK	Digital System Design with VHDL
14	30.05.2026 (Saturday) to 31.05.2026 (Sunday)	Assignment Execution	
15	01.06.2026 (Monday)	Dr. T. Kalavathi Devi Kongu Engineering College, Erode	Microelectronics & VLSI Design
16	02.06.2026 (Tuesday)	Dr. Khaleelu Rahman CBIT Hyderabad	Embedded System Design & IoT
17	03.06.2026 (Wednesday)	Dr. Vijay Nath BIT Mesra	Chip Design
18	04.06.2026 (Thursday)	Dr. A. Kalaivani Rajalakshmi Engineering College, Chennai	Cloud Computing & Distributed Systems & Machine Learning
19	05.06.2026 (Friday)	Dr. Pasuluri Bindu Swetha Ravindra College of engineering for Women, Kurnool	Digital System Design
20	06.06.2026 (Saturday) to 07.06.2026 (Sunday)	Assignment Execution	
21	08.06.2026 (Monday)	Sh. Shreyaskumar Patel NetScout Systems, USA	Embedded System Design
22	09.06.2026 (Tuesday)	Dr. Vijay Nath BIT Mesra	Chip Design & Digital System Design with Verilog



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23	10.06.2026 (Wednesday)	Dr. A. Kalaivani Rajalakshmi Engineering College, Chennai	Cloud Computing & Distributed Systems & Machine Learning
24	11.06.2026 (Thursday)	Dr. Shasanka Sekhar Rout GLA Univ. Mathura	Microelectronics & VLSI Design
25	12.06.2026 (Friday)	Dr. Adesh Kumar UPES, UK	Digital System Design with VHDL
26	13.06.2026 (Saturday) to 14.06.2026 (Sunday)	Assignment Execution	
27	15.06.2026 (Monday)	Module Test + Research Assignment + Doubt Clearance and Feedback	
ISVE Summer Internship Phase-2 will be started from 15 th July 2026 to 14 th August 2026 and same will continue for Phase-3 and Phase-4			
Further program will be executed accordingly, and the next schedule will be display			

ISVE Summer Research Internship-2026

Organizing Committee